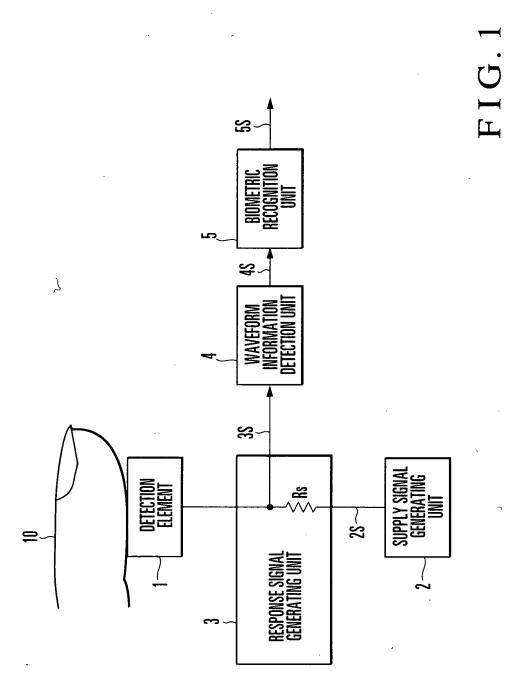
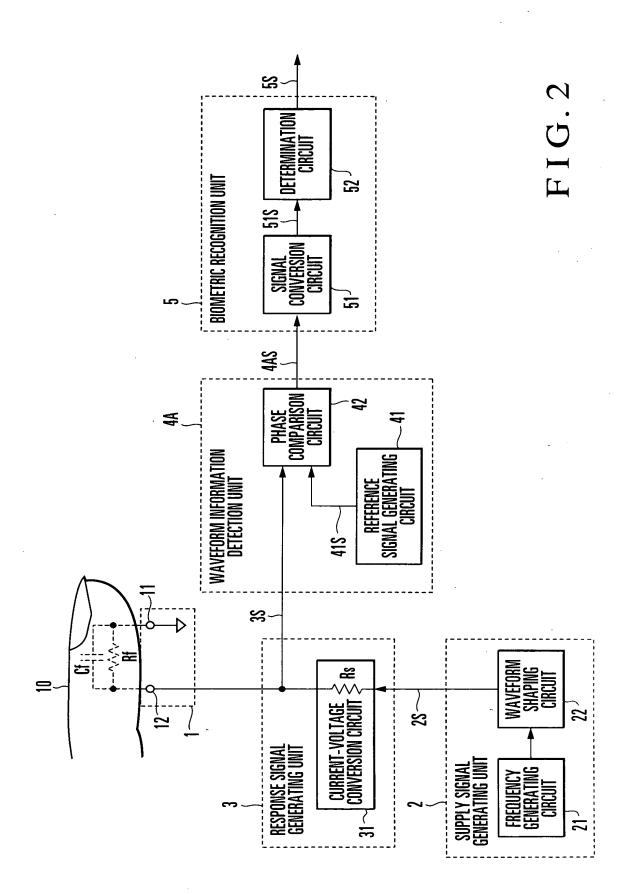
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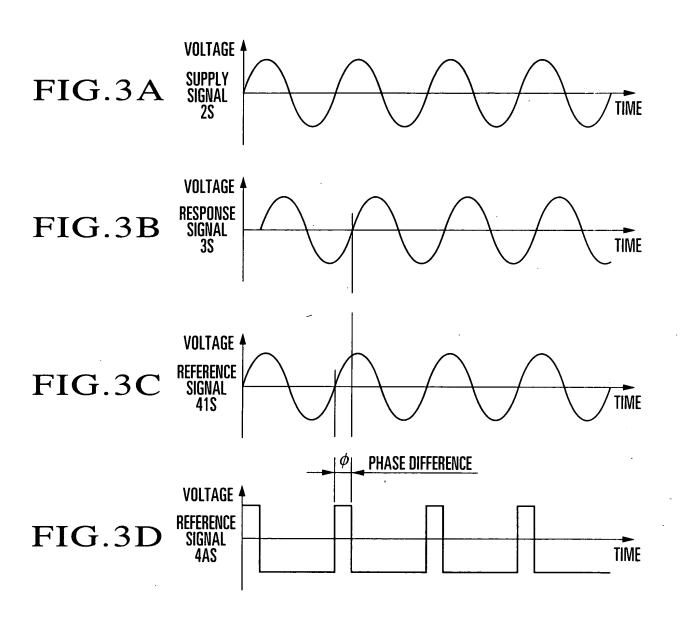


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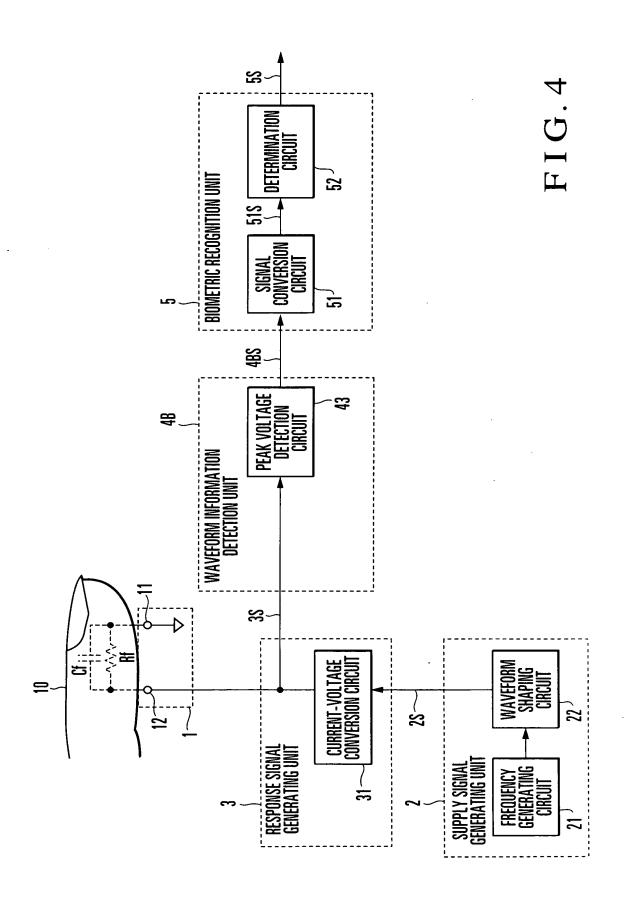
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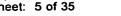


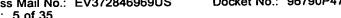
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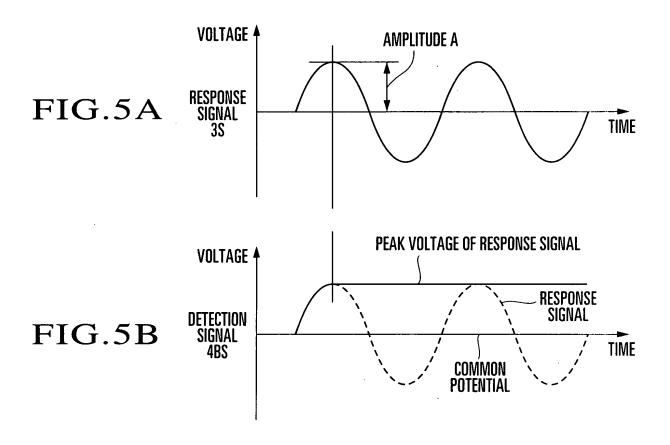


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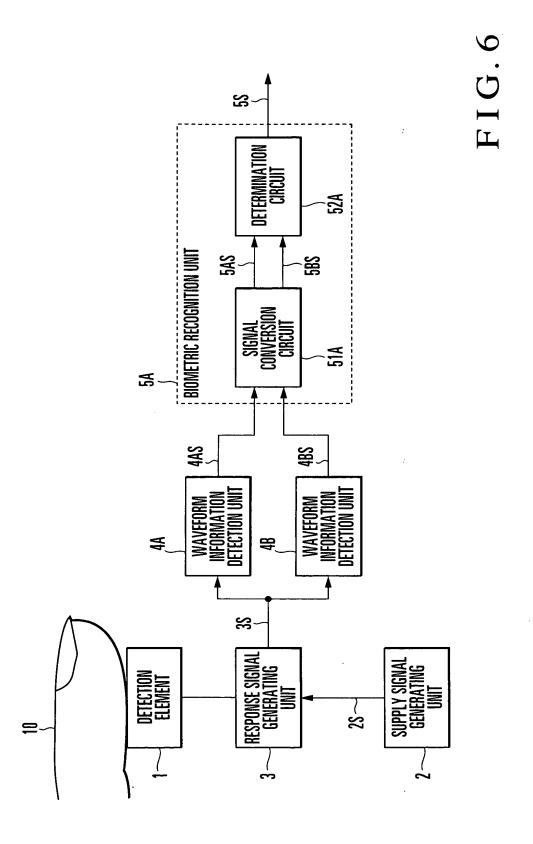
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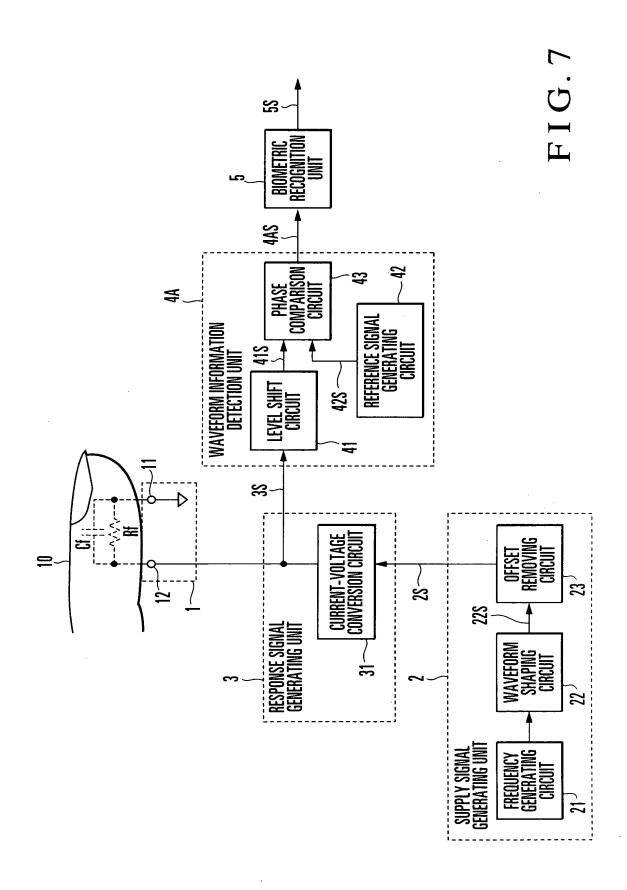




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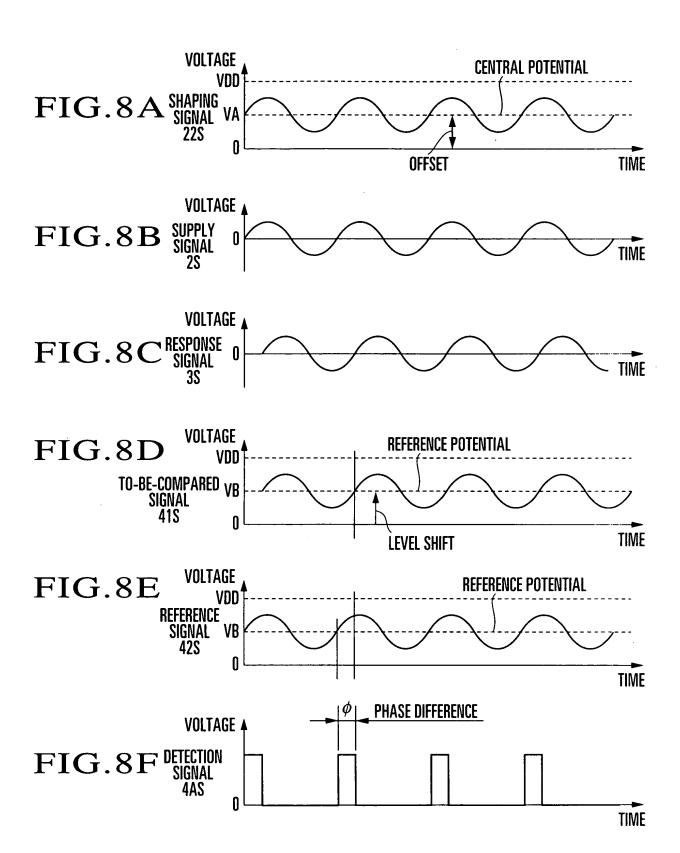
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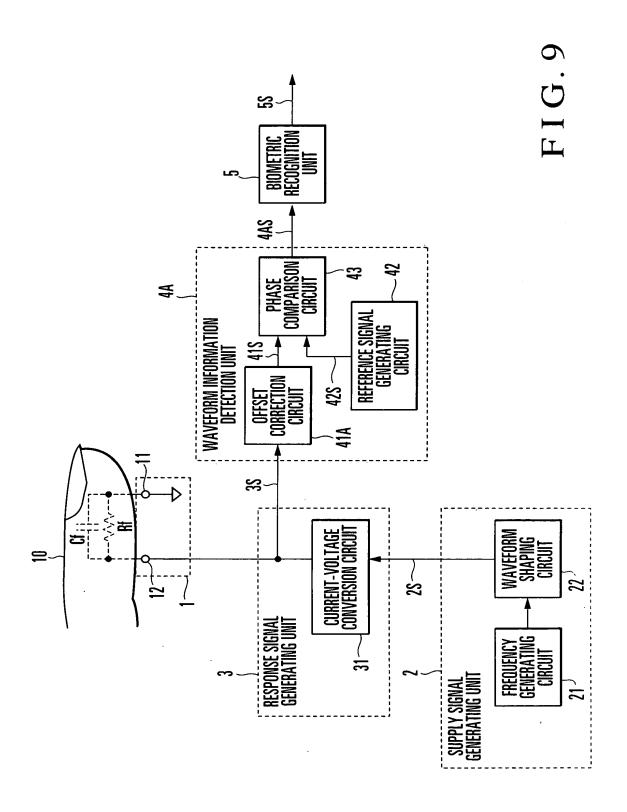
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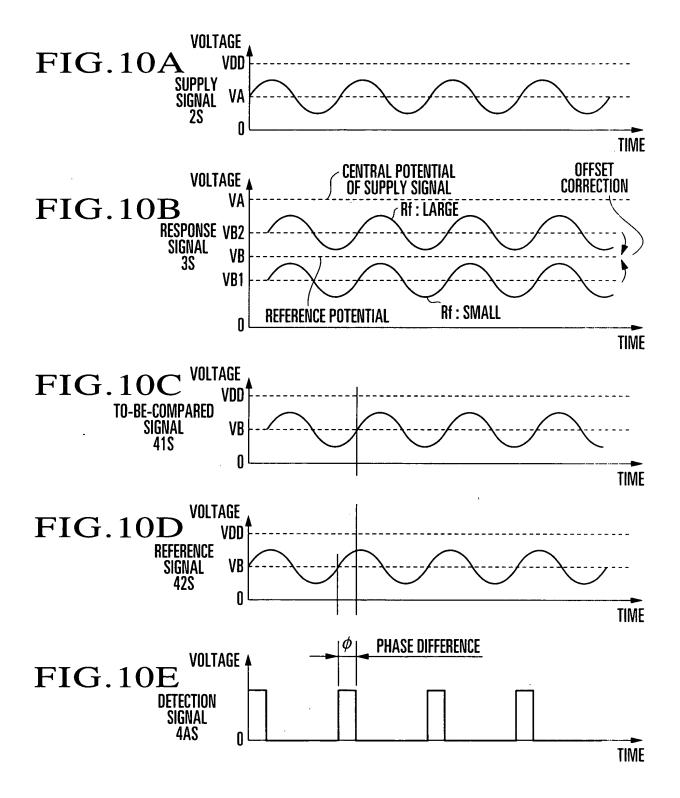
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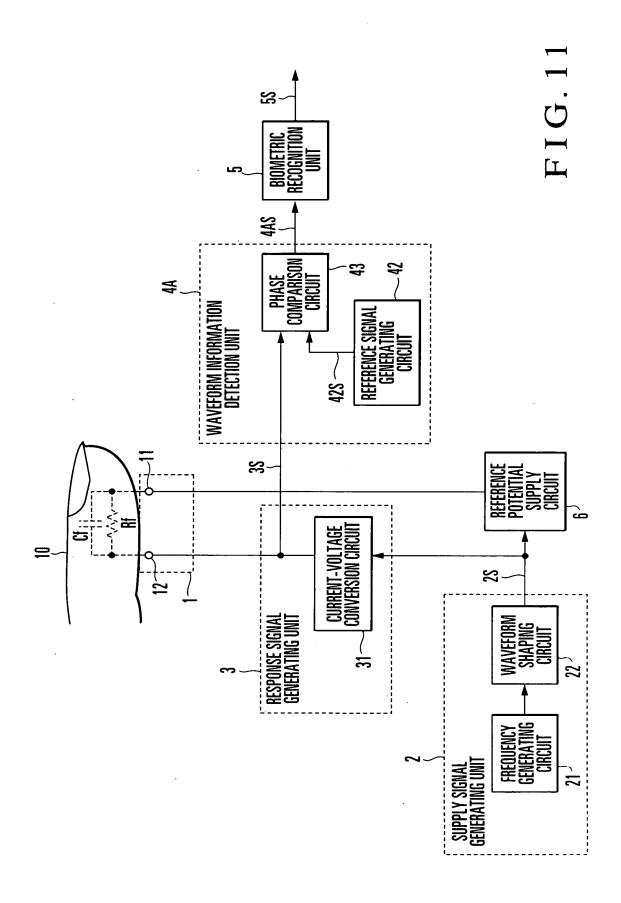
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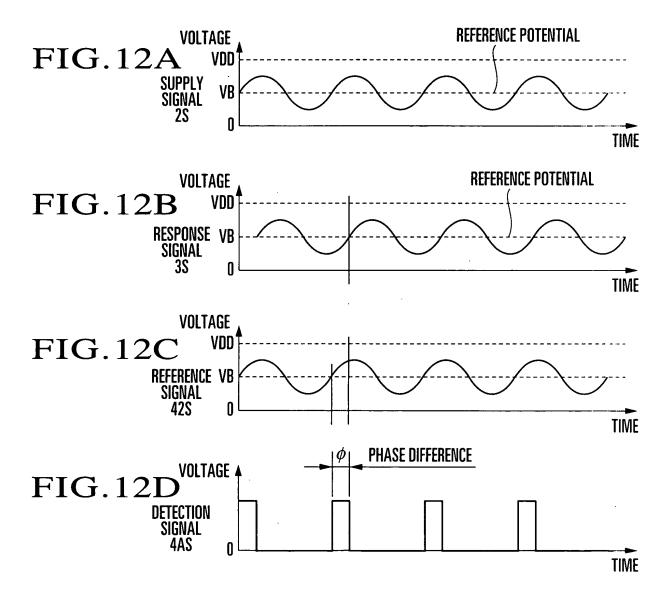
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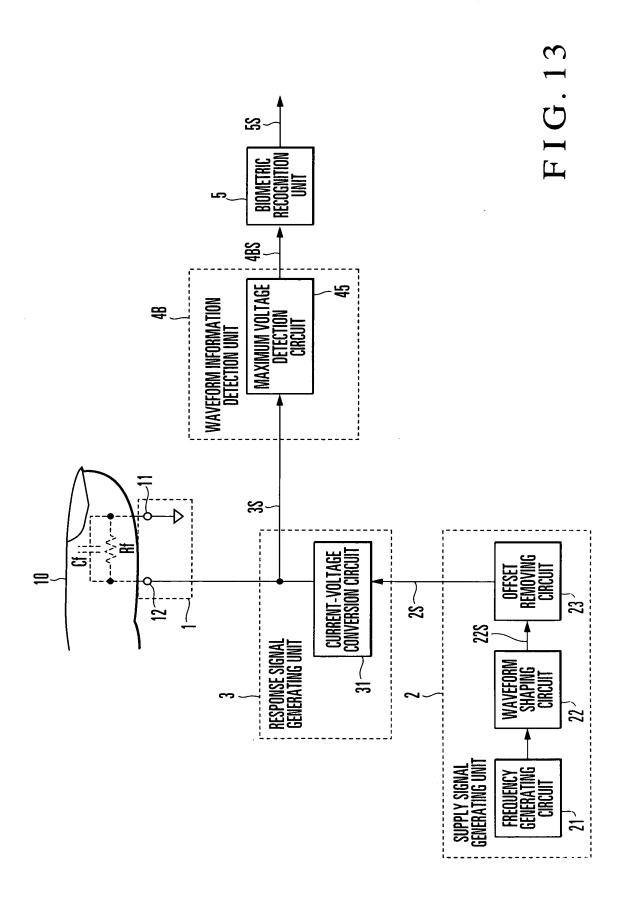


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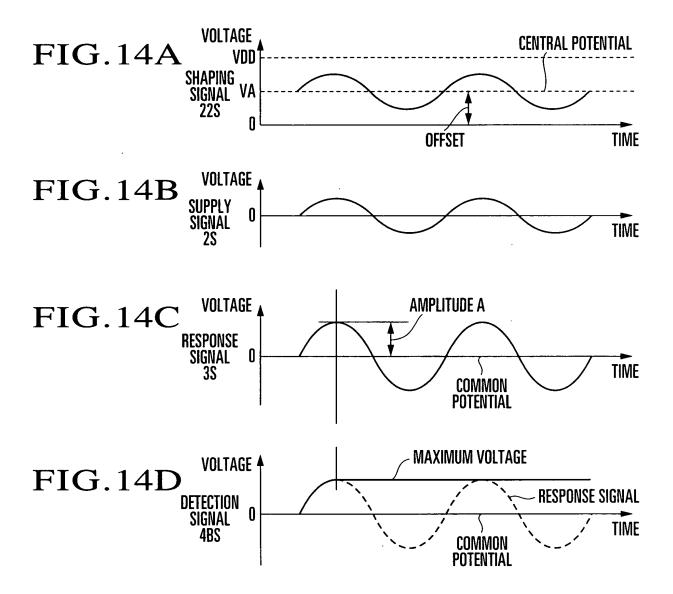
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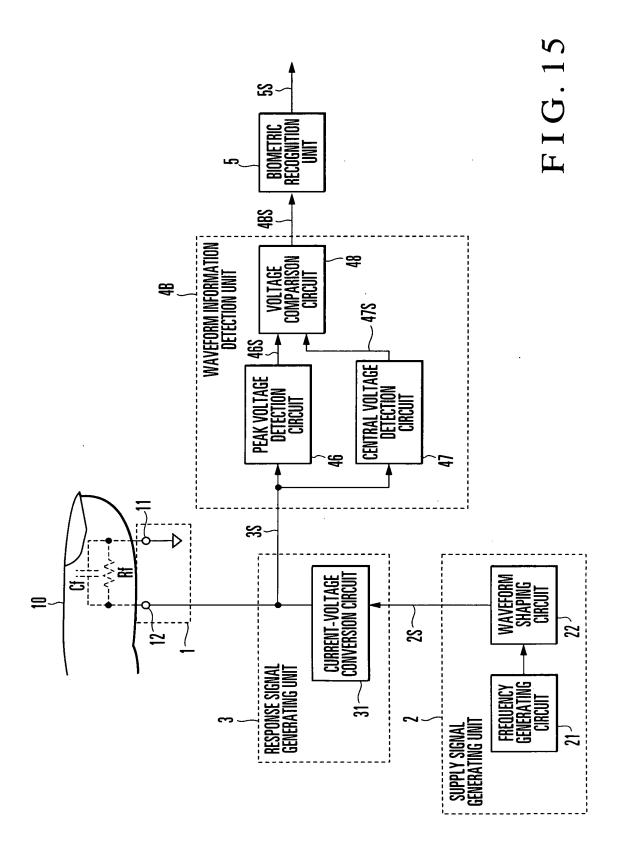
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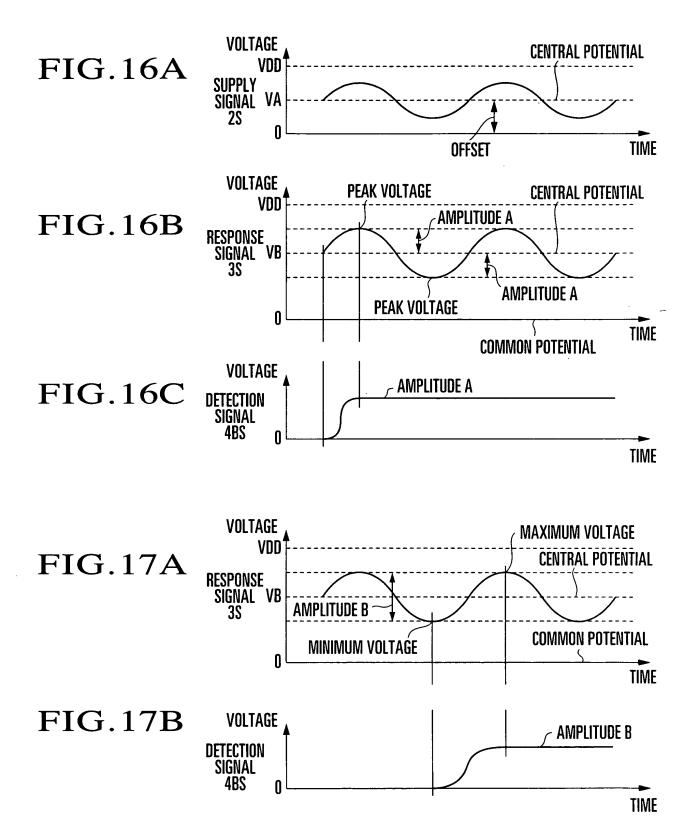


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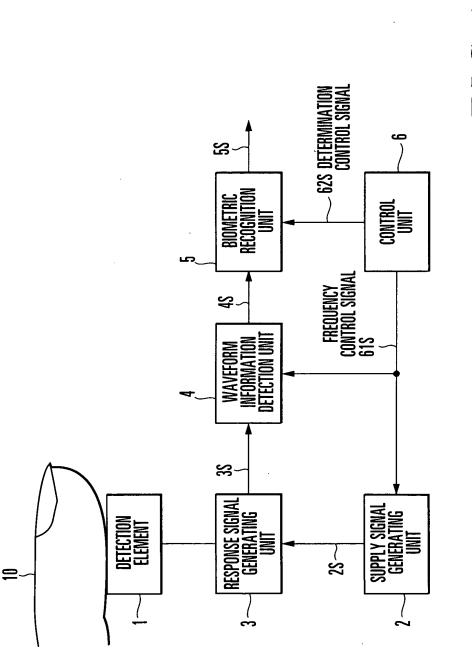


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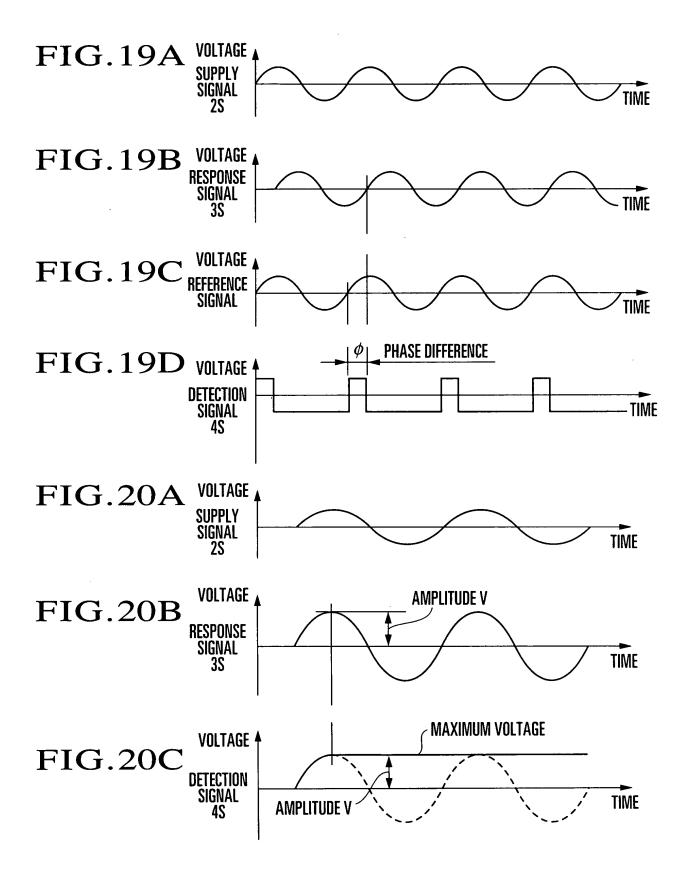


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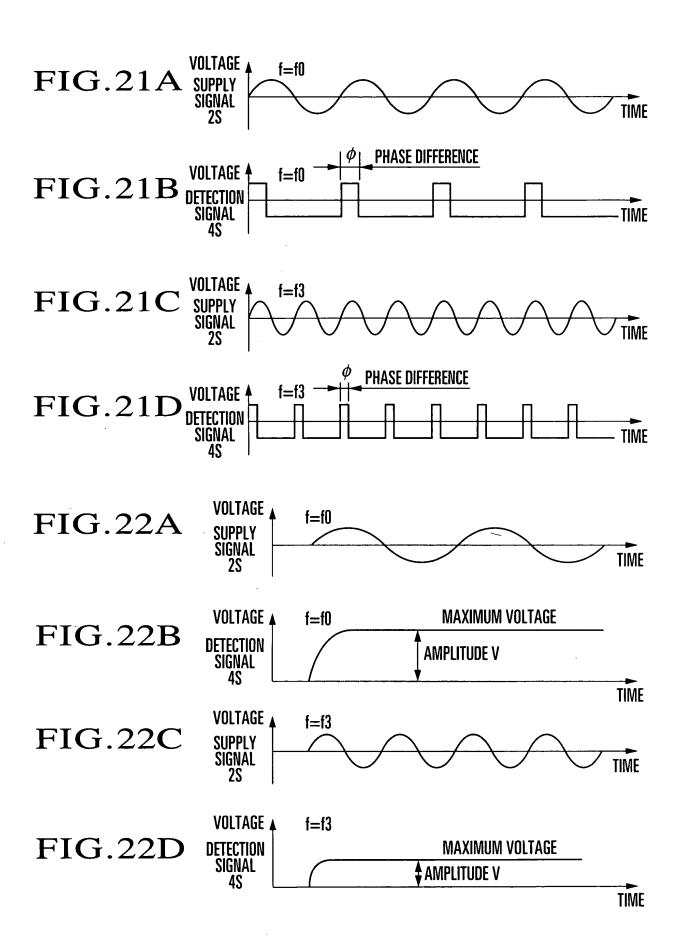
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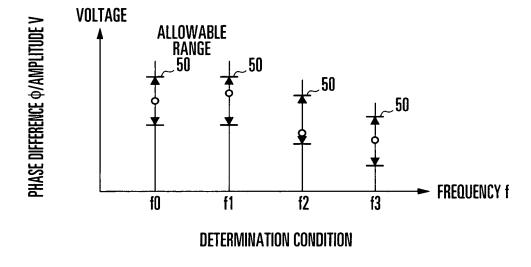
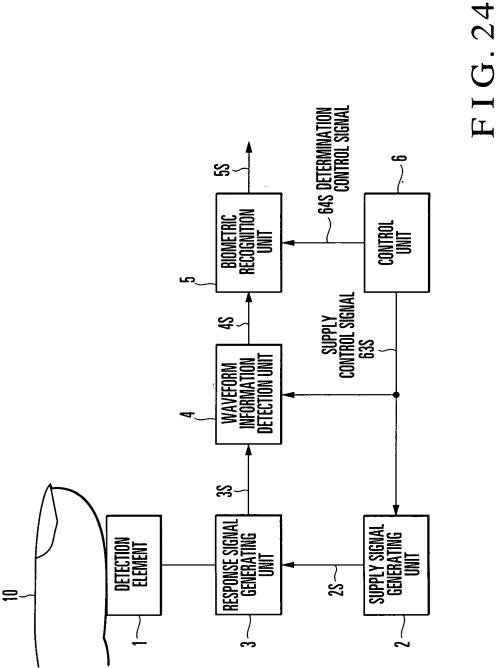


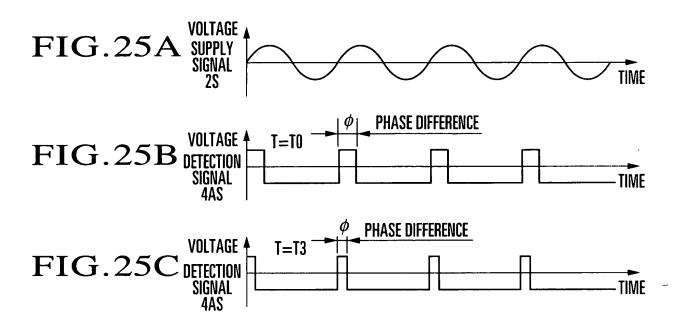
FIG.23

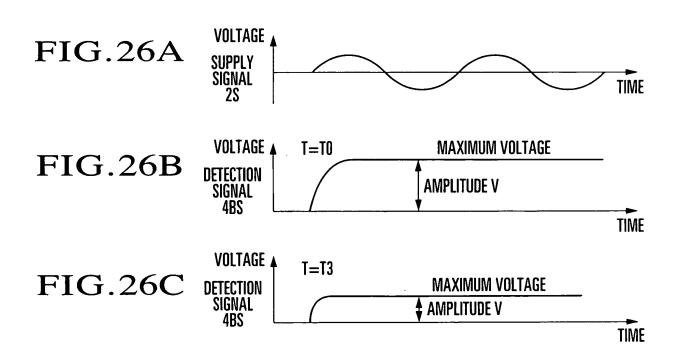
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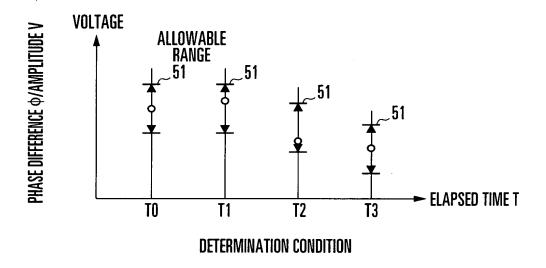
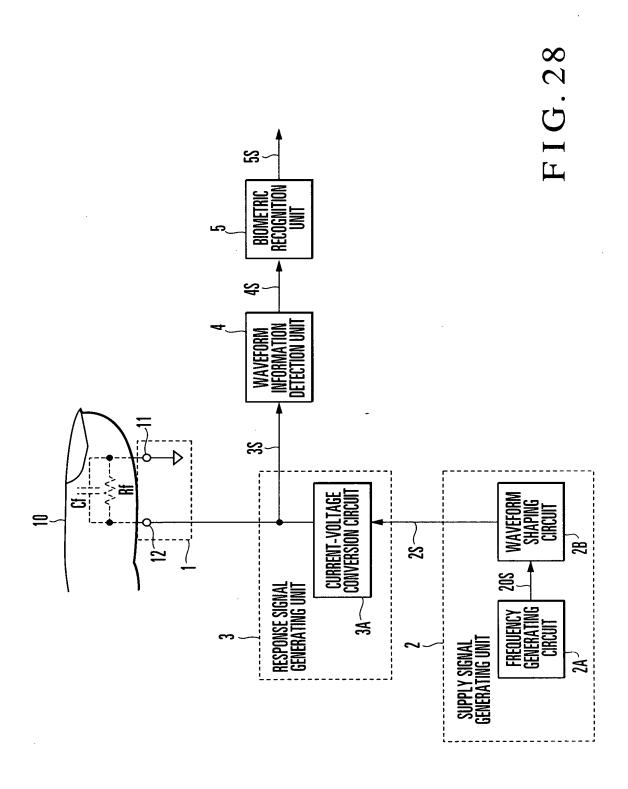


FIG.27

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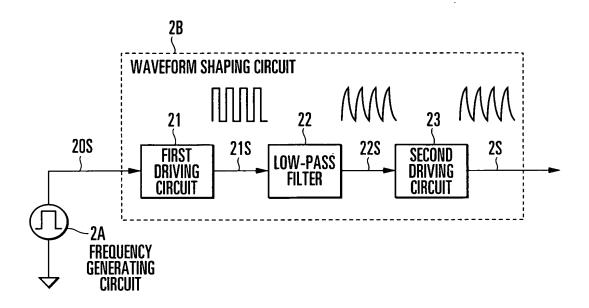


FIG. 29

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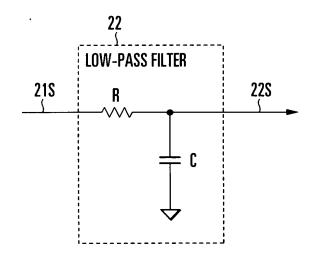


FIG. 30

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2B **WAVEFORM SHAPING CIRCUIT** \mathbf{U} 21 24 **20S** 218 **24S** AMPLITUDE LIMITING FIRST Driving **CIRCUIT CIRCUIT** -2A **FREQUENCY** GENERATING CIRCUIT 23 25 2S SECOND DRIVING **AMPLIFICATION** LOW-PASS FILTER CIRCUIT

FIG. 31

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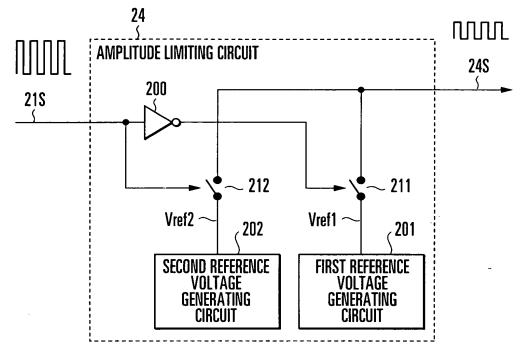


FIG. 32

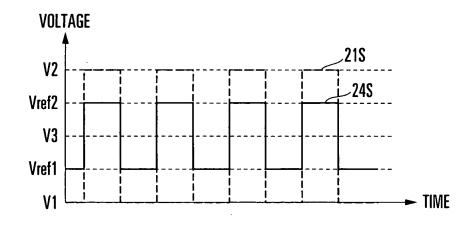


FIG. 33

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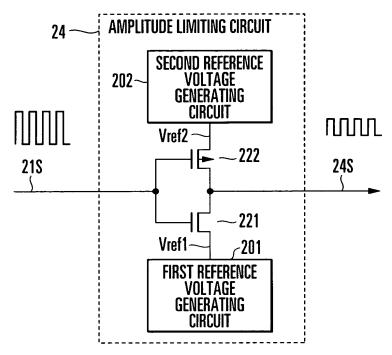


FIG. 34

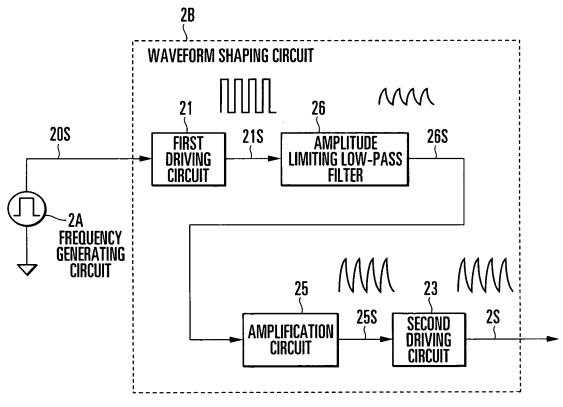


FIG. 35

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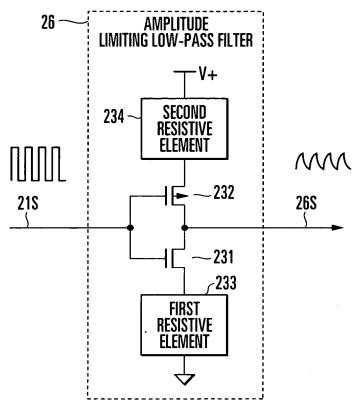


FIG. 36

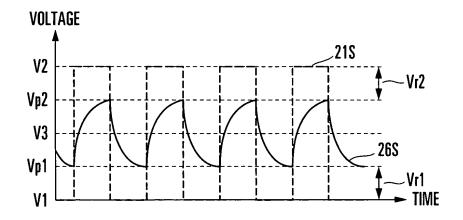


FIG. 37

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26 **AMPLITUDE LIMITING LOW-PASS FILTER** MM241 242 21S **26S** Vref1 -Vref2 -**~201** 202 FIRST REFERENCE VOLTAGE GENERATING **SECOND REFERENCE** VOLTAGE GENERATING **CIRCUIT CIRCUIT**

FIG. 38

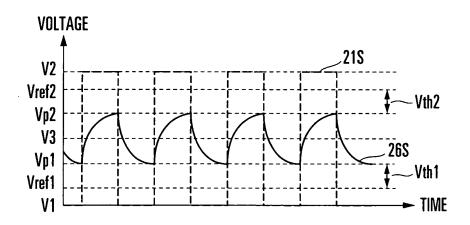
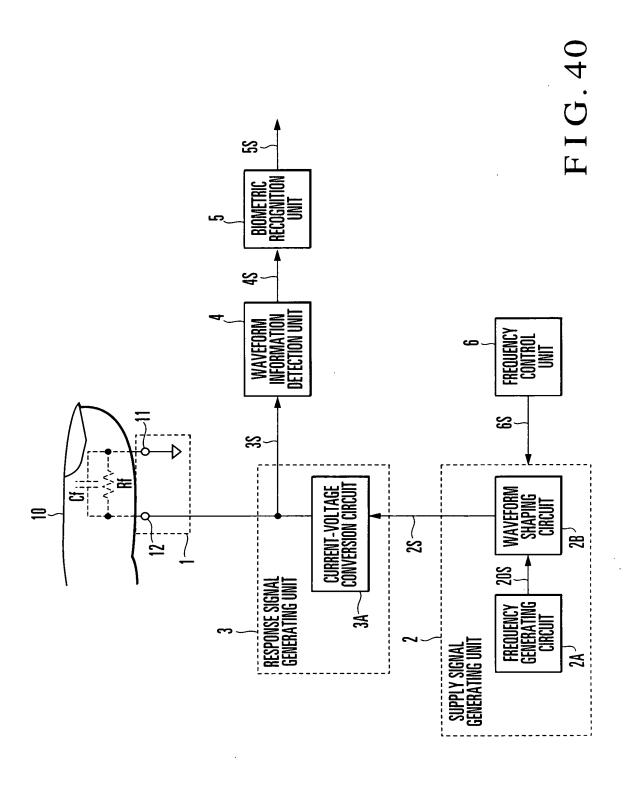


FIG. 39

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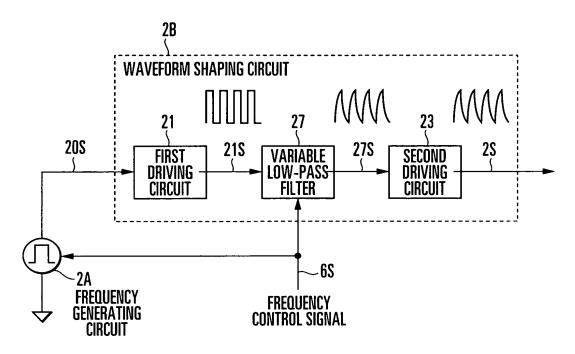


FIG. 41

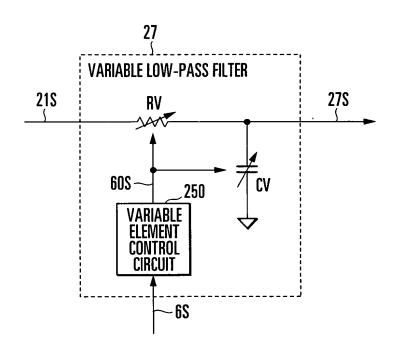


FIG. 42

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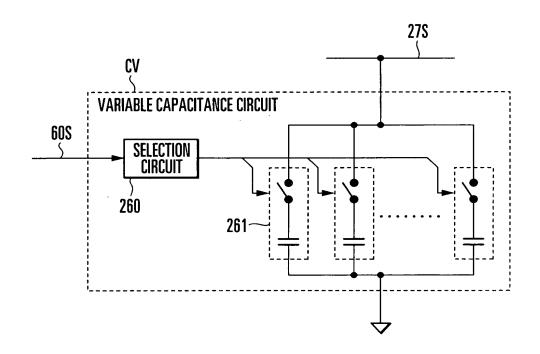


FIG. 43

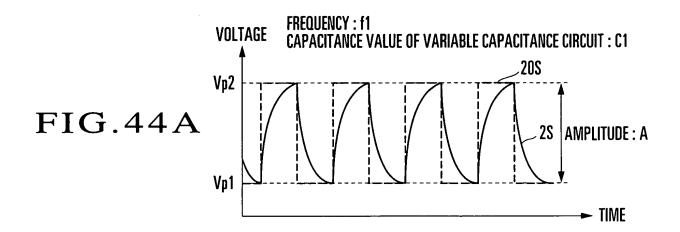
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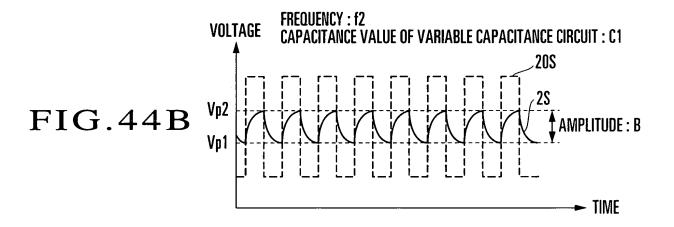
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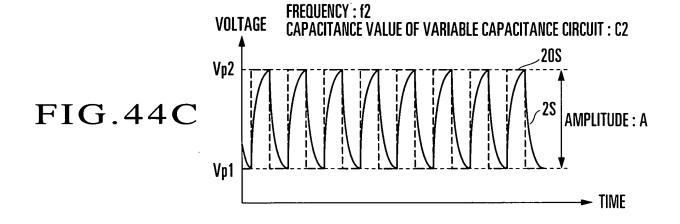
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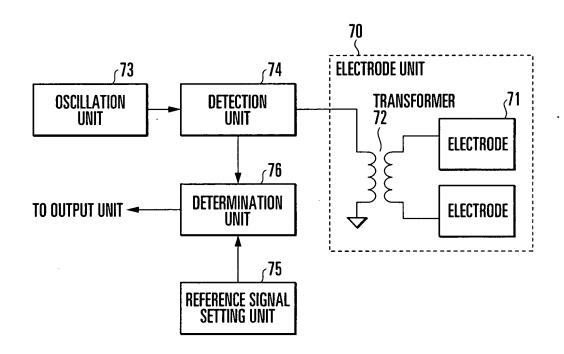


FIG. 45